

DATA SEQUENCE CONVERSION CIRCUIT AND  
PRINTER USING THE SAME

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ABSTRACT OF THE DISCLOSURE

Disclosed is a data sequence conversion circuit for  
10 converting a plurality of input data sequences having  
different data widths into output data sequences having a  
prescribed data width without converting the input data  
sequences into serial data. The data sequence conversion  
circuit, which takes as an input any one of a plurality  
15 of input data sequences having different data widths and  
converts the input data sequence into an output data  
sequence having a prescribed data width for output,  
includes: a first parallel shift register for holding the  
input data sequence; a switch matrix for taking the data  
20 held in the first parallel shift register as input data,  
and for outputting the input data in distributing fashion  
in accordance with a rule selected by a control signal  
from a plurality of predetermined rules; and a second  
parallel shift register for taking the data output from  
25 the switch matrix as input data, and for outputting the  
input data as a data sequence having the prescribed data  
width.